

# **Microelectronic Circuit Design 4th Edition Solution**

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Updated 4/4/14 P11.126 Change the open-loop gain to  
94 dB and unity-gain frequency to 2.5 MHz P13.18  
Should refer to Fig. P13.3 Prob. 13.34 Assume  $V_{SS} = 0$ .  
Fig. P13.13: Transistor should be a depletion-mode  
transistor Prob. 13.40(b) should refer to Fig.  
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\_\_\_\_\_ EX1.8  $(V) V_{PS} = I_D R + V_D$  and  $I_D \cong I_S \exp \left| \frac{V_D}{V_T} \right| (4 - V_D)$  so  $4 = I_D (4 \times 10^3) + V_D \Rightarrow I_D = 4 \times 10^{-3}$  and  $(V) I_D = (10^{-12}) \exp \left| \frac{V_D}{0.026} \right|$  By trial and error, we find  $I_D \cong 0.866$  mA and  $V_D \cong 0.535$  V.

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